

EE 331 Digital Systems with HDL

OIT Portland West, Winter 2012

Assignments for Week 10, March 14

Lecture

To be determined

Reading Assignment

To be determined

Homework Assignment

From the textbook:

Exercise 8.2: Circular Shift with Component #1

Exercise 8.5: Adder with COMPONENT and GENERATE

Also:

Design the generic $M \times N$ circular shifter of example 8.4 so as not to use components but to generate all the necessary logic and registers within a single PROCESS by using the LOOP construct.

Lab Assignment

Work on Lab 5.

Lab 5 is due Next week, March 21.