

EE 331 Digital Systems with HDL

OIT Portland West, Winter 2012

Assignments for Week 3, January 25

Lecture

The lecture will be concurrent code examples in chapter 5. There will be class examples.

Reading Assignment

Chapter 4 - You can ignore any operators introduced in VHDL 2008. See figure 4.1. Skim 4.3 through 4.6 but note that S'EVENT for SIGNALs is very common. Synthesis attributes won't be important for the course but are for large and complex designs. Unfortunately, they tend to be dependent on the synthesizer being used. Skim 4.7, 4.8, ignore 4.9.

Chapter 5 - All but 5.10

Homework Assignment

None because lab assignment is due.

Lab Assignment

Lab 1 is due!

It certainly wouldn't hurt if you have already started on lab assignment 2, which is due next week.