

EE 432 Advanced Digital Design with HDL

Spring 2013

Week 4

Lecture Topic and Reading Assignment for today

At last we will look at the Xilinx FPGA structure.

Check out (supplied on the DVD image from EE331):

Spartan-6/Family Overview.pdf (The part we use is highlighted)

Spartan-6/CLB Users Guide.pdf

Spartan-6/SelectIO Resources User Guide.pdf

Homework Assignment, Due April 30 (next week)

None! Work on the project.

Current Project Assignment

Continue working on the project